

SLEEP RECOVERY CIRCUIT AND METHOD

ABSTRACT

Systems and methods of recovering a device from a sleep mode of operation are described. In one aspect, a device includes a sleep recovery circuit
5 that is operable to transition from a first signal detection mode to a second signal detection mode in response to detection of a first signal characteristic in an input signal. The sleep recovery circuit also is operable to transition from the second signal detection mode to a third operational mode in response to detection in the input signal of a second signal characteristic different from the first signal
10 characteristic.